

TOP SECRET

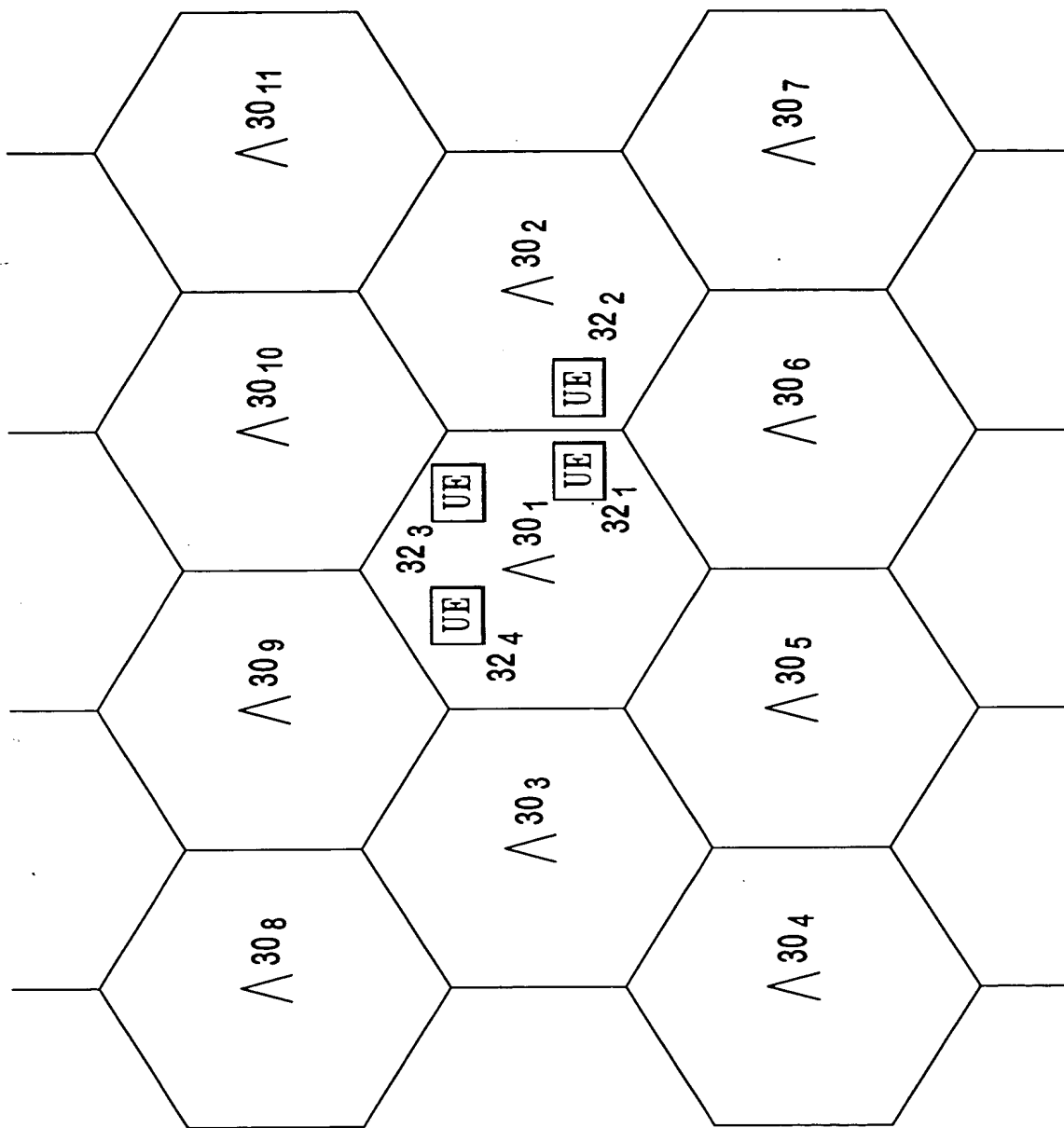
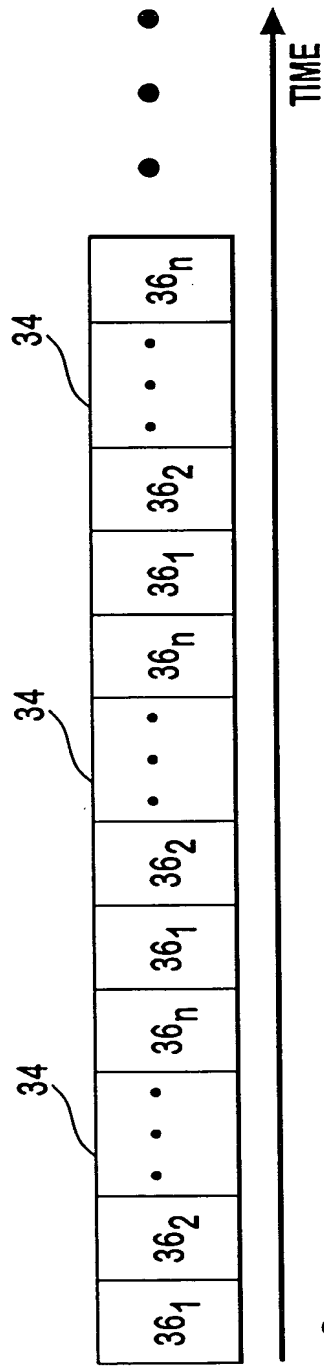


FIG. 1



**FIG. 2**

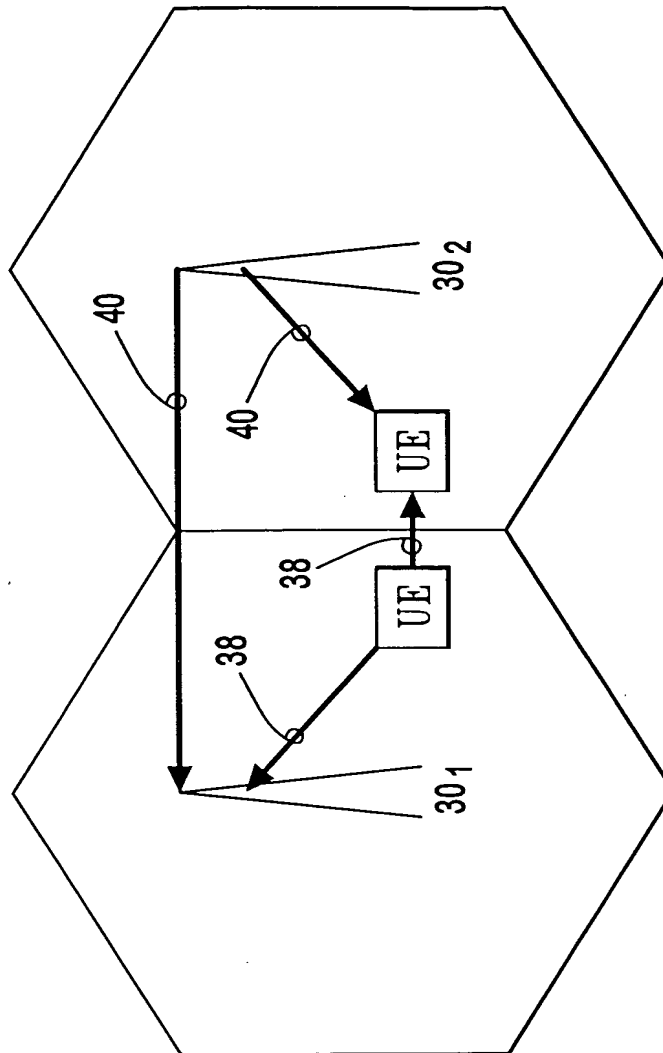


Fig. 3

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CROSS INTERFERENCE CELL LIST											
CELL	POTENTIAL CROSS INTERFERING CELLS										
	1	2	3	4	5	6	7	8	9	10	11
1	X	I	I		I	I*			I	I	
2	I	X				I	I			I	I
3	I		X	I	I*			I	I		
4			I	X	I						
5	I		I*	I	X	I					
6	I*	I			I	X	I				
7		I				I	X				
8			I					X	I		
9	I		I					I	X	I	
10	I	I							I	X	I
11		I								I	X

FIG. 6

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TIME SLOT ALLOCATION PER CELL															
CELL	TIME SLOT ALLOCATION														
	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13	S14	S15
1			D	U											
2	D	D							U						
3				U	D										
4										D	D				
5			D											U	
6						D									U
7										D					
8										D	D				
9						D	U								
10			D	U											
11										D	D	D	D		U

FIG. 7

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AVAILABILITY LIST FOR CELL 1															
CELL (UPLINK / DOWNLINK)	TIME SLOT AVAILABILITY														
	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13	S14	S15
1 (UPLINK)	X	X	X		X	X									
1 (DOWNLINK)				X			X		X					X	X

FIG. 8

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AVAILABILITY LIST				
CELL (UPLINK / DOWNLINK)	TIME SLOT AVAILABILITY			
	S1	S2	...	SN
1 (UPLINK)	X	X	...	
1 (DOWNLINK)		X	...	X
2 (UPLINK)	X	X	...	
2 (DOWNLINK)			...	X
			...	
			...	
			...	
N (UPLINK)	X	X	...	
N (DOWNLINK)		X	...	X

FIG. 4

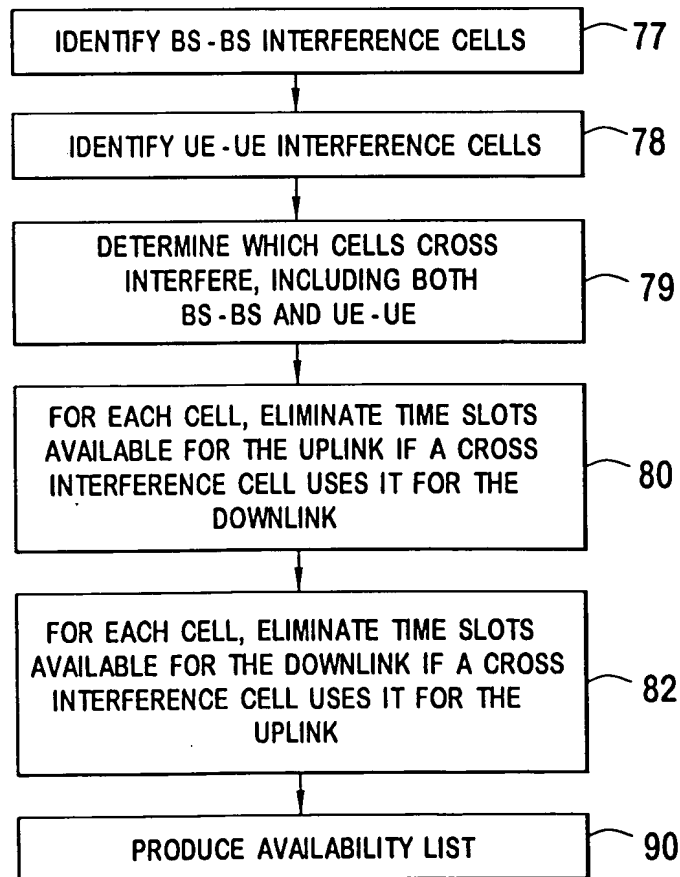


FIG. 5

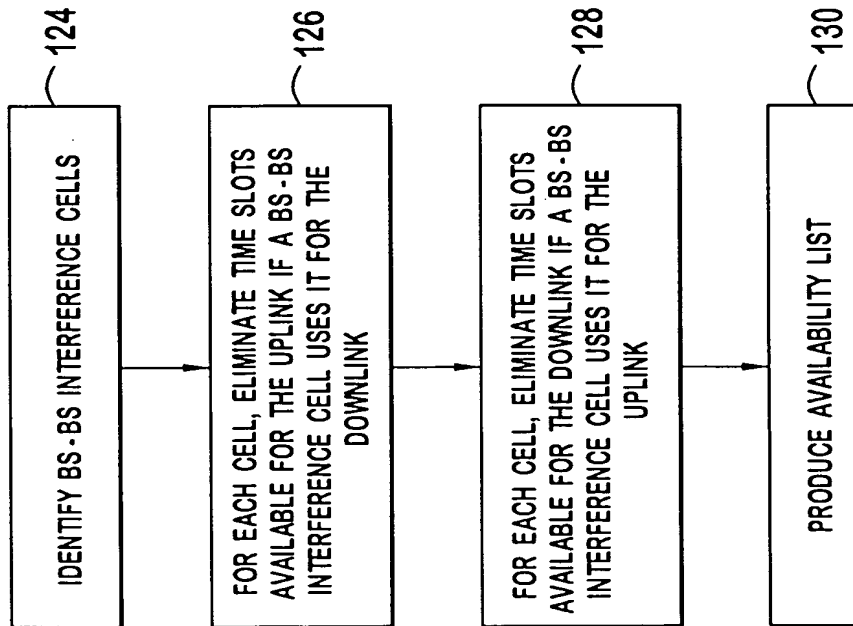


FIG. 9

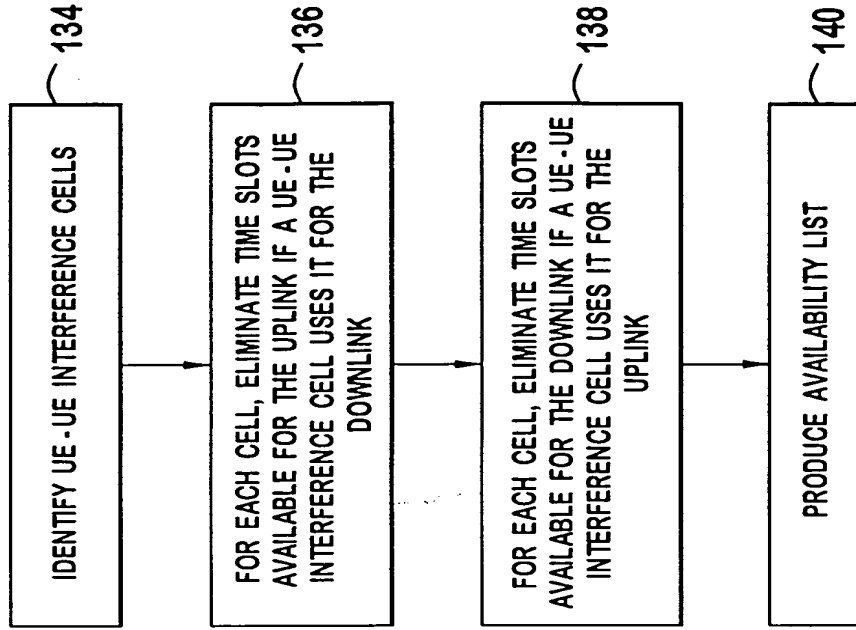


FIG. 11

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BS - BS CROSS INTERFERENCE CELL LIST											
CELL	POTENTIAL CROSS INTERFERING CELLS										
	1	2	3	4	5	6	7	8	9	10	11
1	X	I	I		I				I	I	
2	I	X				I	I			I	I
3	I		X	I				I	I		
4			I	X	I						
5	I			I	X	I					
6		I			I	X	I				
7		I				I	X				
8			I					X	I		
9	I		I					I	X	I	
10	I	I							I	X	I
11		I								I	X

FIG. 10

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UE - UE CROSS INTERFERENCE CELL LIST											
CELL	POTENTIAL CROSS INTERFERING CELLS										
	1	2	3	4	5	6	7	8	9	10	11
1	X	I*	I*		I*	I*			I*	I*	
2	I*	X				I*	I*			I*	I*
3	I*		X	I*	I*			I*	I*		
4			I*	X	I*						
5	I*		I*	I*	X	I*					
6	I*	I*			I*	X	I*				
7		I*				I*	X				
8			I*					X	I*		
9	I*		I*					I*	X	I*	
10	I*	I*							I*	X	I*
11		I*								I*	X

FIG. 12

FIG. 10

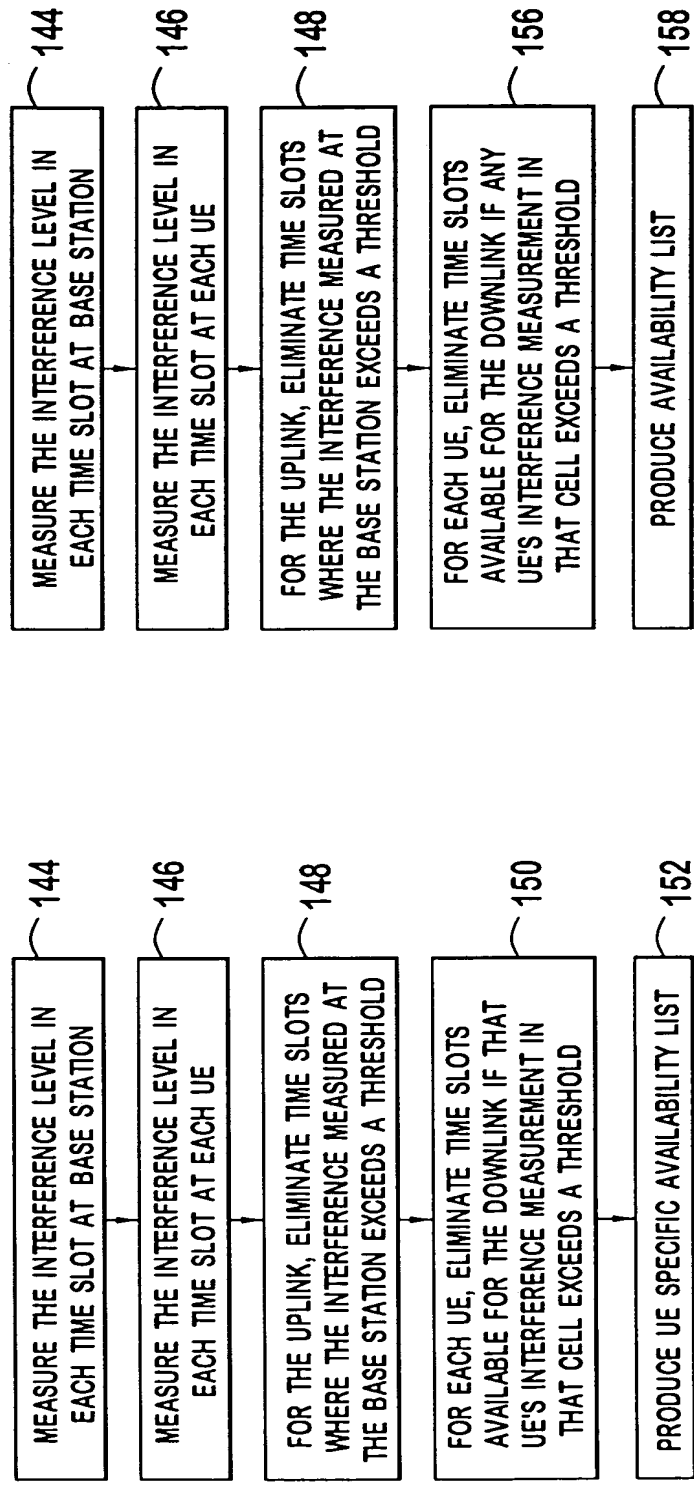


FIG. 13

FIG. 14

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AVAILABILITY LIST FOR CELL N															
UE IN CELL N (UPLINK / DOWNLINK)		TIME SLOT AVAILABILITY													
		S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13	S14 S15
CELL (UPLINK)		X	X					X							
UE 1 (DOWNLINK)				X						X				X	X
UE 2 (DOWNLINK)										X					X
...															
UE N (DOWNLINK)							X	X	X	X					X

FIG. 15

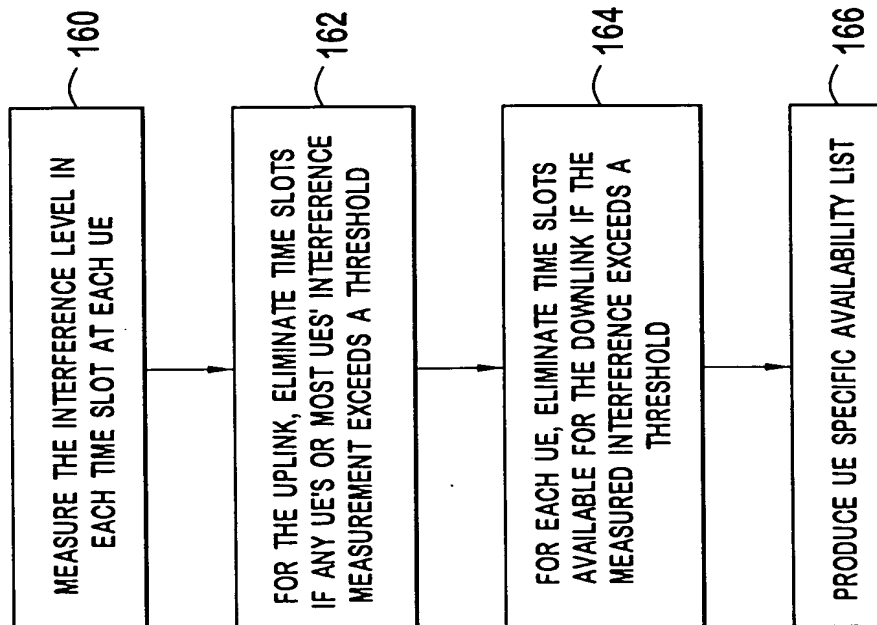


FIG. 16

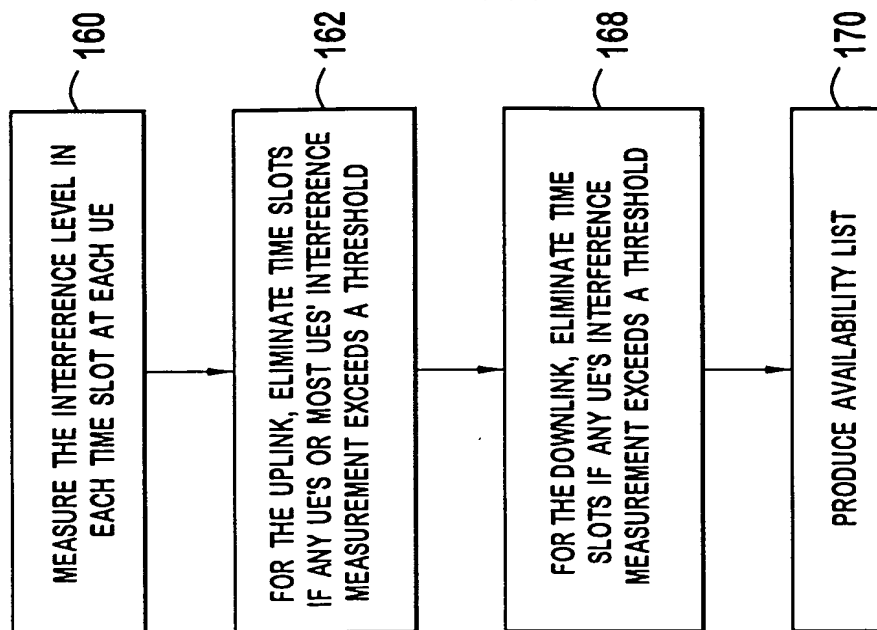


FIG. 17



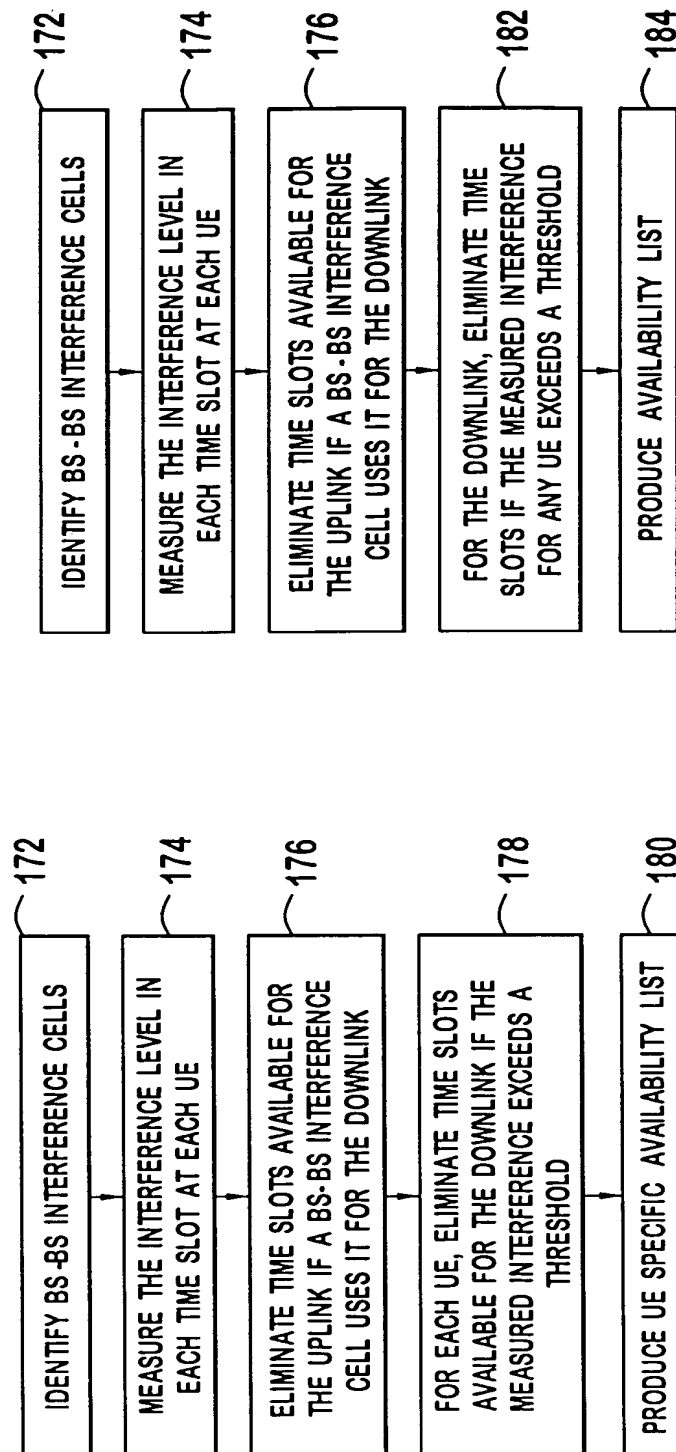


FIG. 18

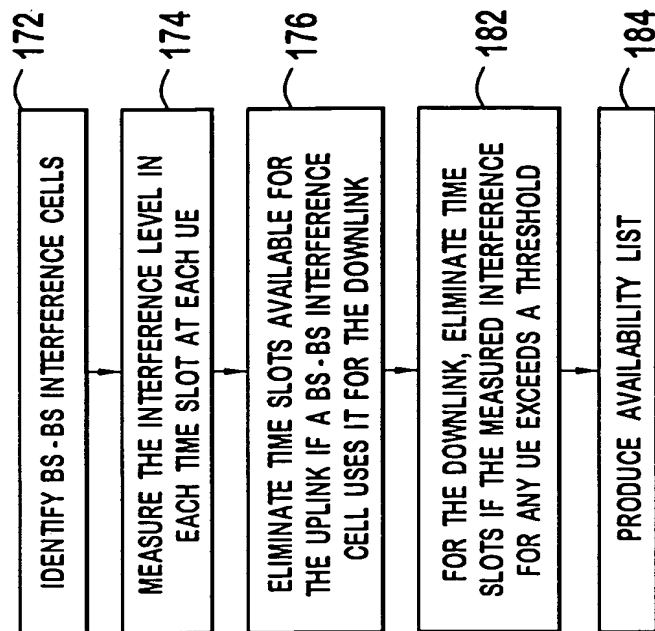


FIG. 19

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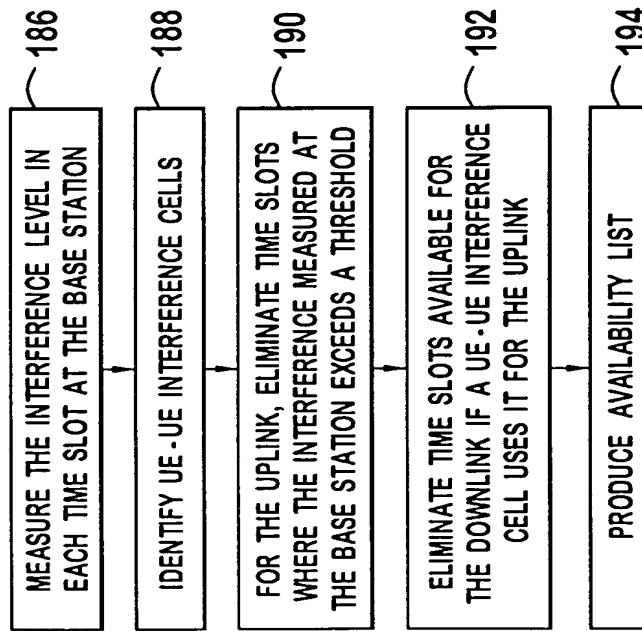


FIG. 20

FIG. 21

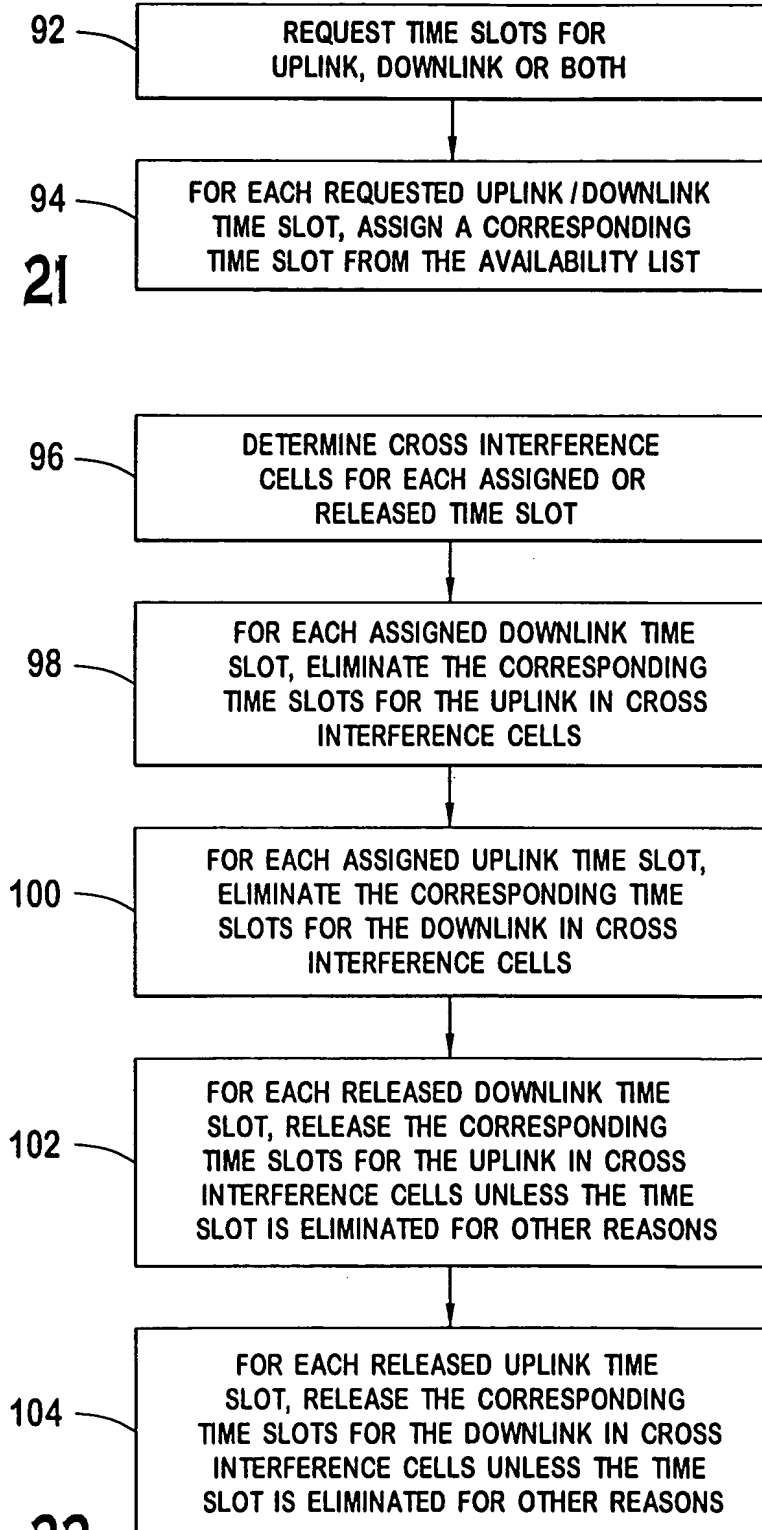


FIG. 22

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TIME SLOT ALLOCATION PER CELL															
CELL	TIME SLOT ALLOCATION														
	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13	S14	S15
1			D	U											
2	D	D							U						
3				U	D										
4										D	D				
5			D											U	
6						D**	D*	U*							U
7										D					
8										D	D				
9						D	U								
10			D	U											
11										D	D	D	D		U

FIG. 23

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AVAILABILITY LIST FOR CELL 7															
CELL (UPLINK / DOWNLINK)	TIME SLOT AVAILABILITY														
	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13	S14	S15
7 (UPLINK)	X	X				R	X*								
7 (DOWNLINK)								X*	X						X

FIG. 24

FIG. 23

FIG. 25

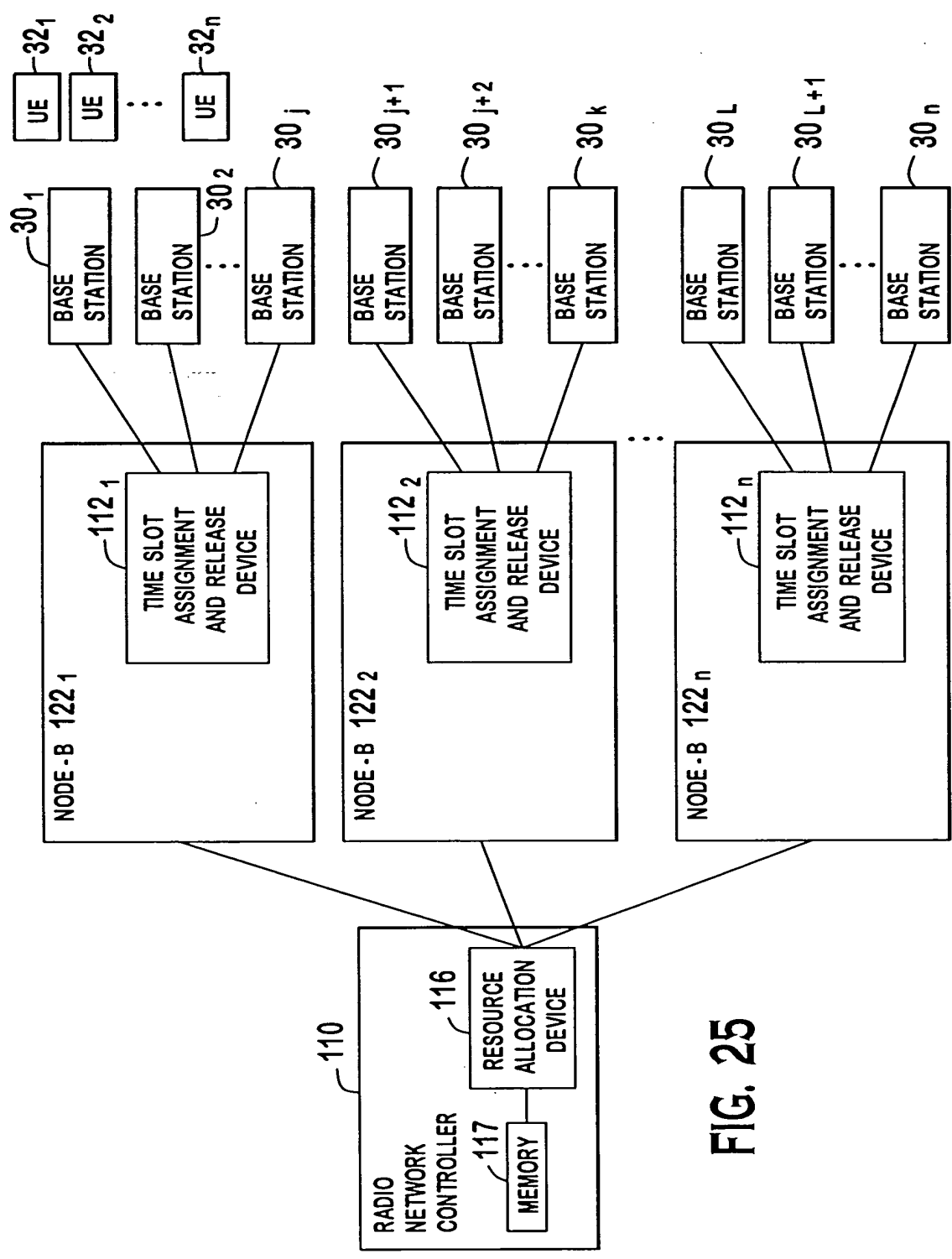


FIG. 25

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FIG. 26

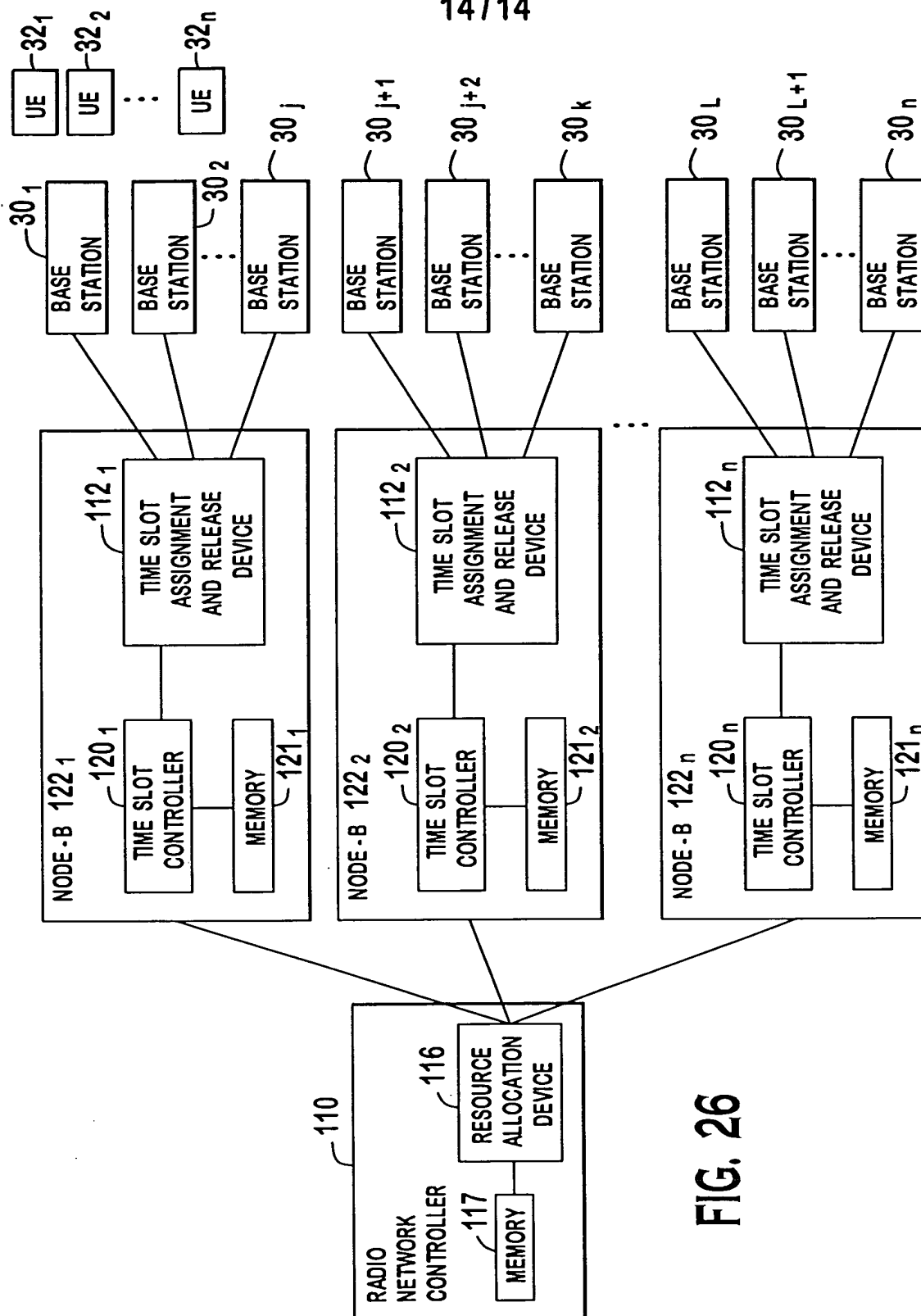


FIG. 26